

**BOARD OF STUDIES (UG)****DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING**

Academic Year : 2023-2024 , 2024-2025, 2025-2026

S. No	Name	Designation & Affiliation	Role
1.	Mr. Y. Rajesh Babu	In-charge HOD Dept. of EEE, KITS, Guntur, AP.	Chairman
2.	Dr. Ch Sai Babu	Professor of EEE Dept., UCE Director Academic Audit, JNTUK, Kakinada, AP.-533003	Subject Expert JNTUK University Nominee
3.	Dr. G. V.Siva Krishna Rao	Professor Dept. of EEE, College of Engineering (A) Andhra University Visakhapatnam, AP-530003.	Subject Expert-I Nominated by the Academic Council
4	Dr. Sankar Peddapati	Assist. Professor Dept. of Electrical Engineering, National Institute of Technology Andhra Pradesh, AP.	Subject Expert-II Nominated by the Academic Council
5	Er.N.Lakshman Rao	Deputy Executive Engineer, Viduth souda, APGenco, Vijayawada, Andhra Pradesh, India	Industrial Expert Nominated by the Academic Council
6	Er.A. Kumar Babu	Assistant Loco Pilot, Nandyal Depot, Guntur Division, South Central Railways	Alumni Member Nominated by the Academic Council
7	Mr.S.Venkatesh	Assist. Professor Dept. of EEE, KITS, Guntur, AP.	Internal Member
8	Mr.M.Raja Nayak	Assist. Professor Dept. of EEE, KITS, Guntur, AP.	Internal Member
9	Mr.M.Naveen Kumar	Assist. Professor Dept. of EEE, KITS, Guntur, AP.	Internal Member
10	Mrs.K.Vasanthi	Assist. Professor Dept. of EEE, KITS, Guntur, AP.	Internal Member
11	Mr.T.Nagaraju	Assist. Professor Dept. of EEE, KITS, Guntur, AP.	Internal Member
12	Mrs.V.Prudvi Raju	Assist. Professor Dept. of EEE, KITS, Guntur, AP.	Internal Member

K. Hari Babu
PRINCIPAL




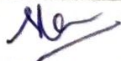

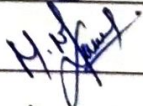




BOARD OF STUDIES MEETING

Department : Electrical and Electronics Engineering
Academic Year : 2023-24(R23) Meeting No. : 01
Date : 13/10/2023 Time : 10.30AM
Venue : ONLINE Board Room

Agenda:

1. Constitution of Board of Studies
2. Introduction of BOS members
3. Views on autonomous academic regulation and course structure
4. Views on course structure of I.B.Tech I sem & II Sem
5. Finalization of I B.Tech 1st and 2nd semester syllabus
6. Any other matter with the permission of the chairperson

Members Attended:

S. No	Name	Role	Signature
1.	Mr. Y. Rajesh Babu	Chairman	
2.	Dr. Ch Sai Babu	Subject Expert from JNTUK	
3.	Dr. G. V.Siva Krishna Rao	Subject Expert-I	
4	Dr. Sankar Peddapati	Subject Expert-II	
5	Er.N.Lakshman Rao	Industrial Expert	
6	Er.A. Kumar Babu	Alumni Member	
7	Mr.S.Venkatesh	Internal Member	
8	Mr.M.Raja Nayak	Internal Member	
9	Mr.M.Naveen Kumar	Internal Member	
10	Mrs.K.Vasanthi	Internal Member	
11	Mr.T.Nagaraju	Internal Member	
12	Mr.V.Prudvi Raju	Internal Member	
Any Special Invites			
13	Dr. Dr.Ravindrababu M	Special Invitee	
14	Dr.Adinarayana,HOD,ECE	Interdisciplinary from Dept. of ECE, KITS	



Department of Electrical and Electronics Engineering

Department: **Electrical and Electronics Engineering**

Event: **Board of Studies meeting**

Date & Venue: **13-10-2023 & Board Room**

Mode: **Online**

Minutes of Ist Board of Studies Meeting

The Chairman of BOS, Department of Electrical and Electronics Engineering (EEE), has welcomed all members and briefed the meeting agenda that has been deliberately considered for discussion.

Agenda-1: Constitution of Board of Studies

The Chairman has briefed the UGC guidelines for the constitution of the BOS of the EEE department.

Agenda-2: Introduction of BOS members

The Chairman has introduced the BOS members.

Agenda-3: Views on autonomous academic regulation

The Chairman has briefed the college academic committee recommendation on a curriculum framework. The Academic committee recommended to follow same R20 Regulation for R23 also.

Agenda-4: Views on course structure of I.B.Tech I sem & II Sem

The Chairman has presented the course structure drafted by the department syllabus framework committee based on stakeholders' feedback analysis. The BOS has finalized the new course structure of I B.Tech for K23 regulation.

Agenda-5: Finalization of I B.Tech 1st and 2nd semester syllabus

The BOS has deeply analyzed the drafted curriculum for K23 regulation, the course content of all subjects of I B.Tech 1st and 2nd semester have appropriately framed. The semester wise changes are:

I semester:

- In the I B.tech all branches have a course Basic of Electrical and Electronics Engineering.

- The syllabus splitted in to two parts A and B consists six units, from that, part A consists three units cover basic electrical engineering concepts (DC and AC circuits fundamentals, DC machines and AC machines, Measuring Instruments and Electrical Energy generation sources and its tariff). The rest is part B consists three units cover basic concepts of electronics engineering (Diodes, Rectifiers and Transistors, Operational Amplifiers, Logic gates and simple combinational and sequential circuits).
 - It is found in the drafted copy that for Electrical and Electronics Engineering laborataty , a topic on study on magnetization characteristics of DC shunt generator is not included. Hence it is included in Unit II.
 - For latest applications of IOT...etc, The knowledge on stepper motor is required for all engineeri9ng graduates. Hence it is included in the second unit.
 - It is found in the drafted copy that for Electrical and Electronics Engineering laborataty , Concept of energy meter is not included , hence in measuring section of second unit it is included to obtain the knowledge on energy consumption in domestic applications.
 - Furthermore, thermal power generation system, batteries and its types are included along with the other conventional and nonconventional energy sources for complete overview on power generation systems.
- The drafted course content of Basic of Electrical and Electronics Engineering is reviewed and approved.

II semester:

- The EEE department offers a course on Electrical Circuit Analysis -I for EEE branch, is reviewed and approved.
- The proposed course name Electrical Circuits Lab for EEE program, is renamed as Electrical Circuits-I Lab. This Electrical Circuits-I Lab offers various electrical circuit related experiments on DC excitation. In continuation with this lab, Electrical Circuits-II Lab is included in IIIrd semester which offers various



electrical circuit related experiments on AC excitation. From the draft copy, study on locus diagram using frequency variable is included additionally along with the L, C variable in 8th experiment. The drafted course content is reviewed and approved.

- The EEE department offers a course on Network Analysis for ECE branch. The course comprises the five units consists fundamental of Electrical circuits, Magnetic circuits, Single phase AC circuits with resonance and its associated locus diagrams, Network theorems with DC and AC excitation.
 - From the draft copy, the concept of variation of locus diagrams with frequency variable is included along with R, L, C variables.
 - Also, nodal and mesh analysis for AC excitation circuits is included in unit five before learning the network theorems.

- The EEE department offers a course on Network Analysis and Simulation Lab for ECE branch. From the draft copy, BOS has decided to remove experiment 1 i.e. verification of KCL and KVL due to repetition in Electrical and Electronics Engineering Workshop Lab. Similarly, Experiment 3 in this Network Analysis and Simulation Lab is splitted in to two experiments, one experiment verification of superposition theorem is added in 1st experiment and the other part remains same as third experiment. The drafted course content is reviewed and approved.

Vinjanampadu

27/03/2021




Chairman

